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DESIGN OF 5.5 GHz HIGHLY LINEAR CMOS LOW NOISE AMPLIFIER



LAP Lambert Academic Publishing Dez 2013, 2013. Taschenbuch. Book Condition: Neu. 220x150x5 mm. This item is printed on demand - Print on Demand Neuware - Recently, telecommunication system requires high performance, low noise, low power and high linear RF circuits. Since the digital modulation design requires highly linear front end circuits, the linearity requirement of the LNA becomes more rigorous. Due to possible large interference signal tones at the receiver end along with the carrier, LNA is expected to provide...

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